

**Notice of References Cited**

Application/Control No.

09/853,005

Applicant(s)/Patent Under  
Reexamination  
TSAI ET AL.

Examiner

Paul A Baker

Art Unit

2187

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*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,182,176	01-2001	Ziegler et al.	710/112
	B	US-6,032,228	02-2000	Islam et al.	711/144
	C	US-			
	D	US-			
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	F	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	IBM TDB "Integrated Concurrency and Shared Buffer Coherency Controls for Multi-Systems", March 1, 1996 Vol 28 Issue 10 pages 4642 - 4650
	V	Dan et al. "Performance Analysis of Buffer Coherency Policies in a Multisystem Data Sharing Environment", IEEE Trans. on Parallel and Distributed Systems" Vol 4, No 3, March 1993
	W	IBM TDB "Hybrid Buffer Coherency Control Scheme", Sept 1, 1991, Vol 34 Issue 4B pages 182 - 184
	X	Handy "The Cache Memory Book", 2 <sup>nd</sup> Edition Academic Press 1998, pg 47-49, 165-169

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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